

**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

Applicants: Taylor et al.	)	I hereby certify that this paper is
	)	being electronically deposited with
Serial No.: 10/829,495	)	the United States Patent and
	)	Trademark Office on this date:
Filed: April 22, 2004	)	
	)	
For: METHOD AND SYSTEM FOR	)	<b>February 12, 2009</b>
FAIL-SAFE RENAMING OF LOGICAL	)	
CIRCUIT IDENTIFIERS FOR	)	
REROUTED LOGICAL CIRCUITS IN A	)	
DATA NETWORK	)	<u>/Felipe Hernandez/</u>
	)	Felipe Hernandez
Group Art Unit: 2419	)	Registration No. 61,971
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Examiner: Ashley L. Shivers	)	

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The documents listed on the attached PTO/SB/08a are submitted pursuant to 37 CFR  
§§ 1.56, 1.97, and 1.98.

## **FEE**

Submitted herewith is a fee of \$180.00 set forth in 37 CFR 1.17(p). See 37 CFR 1.97 (c) (2).

## **METHOD OF PAYMENT**

The fee of \$180.00 is submitted herewith via electronic funds transfer. However, the Commissioner is hereby authorized to charge any fees which may be required to Deposit Account No. 50-2455.

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Respectfully submitted,

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**February 12, 2009**